

FIG. 1 is a block diagram of a system for processing digital images. The system includes a host computer 100, a digital image input device 144, a digital image output device 148, a processor 114, a driver program 118, an appearance enhancement mechanism 150, and a compression enhancement mechanism 160. The host computer 100 is connected to the digital image input device 144 and the digital image output device 148. The processor 114 is connected to the driver program 118, the appearance enhancement mechanism 150, and the compression enhancement mechanism 160. The driver program 118 is connected to the appearance enhancement mechanism 150 and the compression enhancement mechanism 160. The appearance enhancement mechanism 150 is connected to the compression enhancement mechanism 160. The compression enhancement mechanism 160 is connected to the digital image output device 148.

100

OFFICE
MACHINE
(E.G., PRINTER)
120

CABLE 130

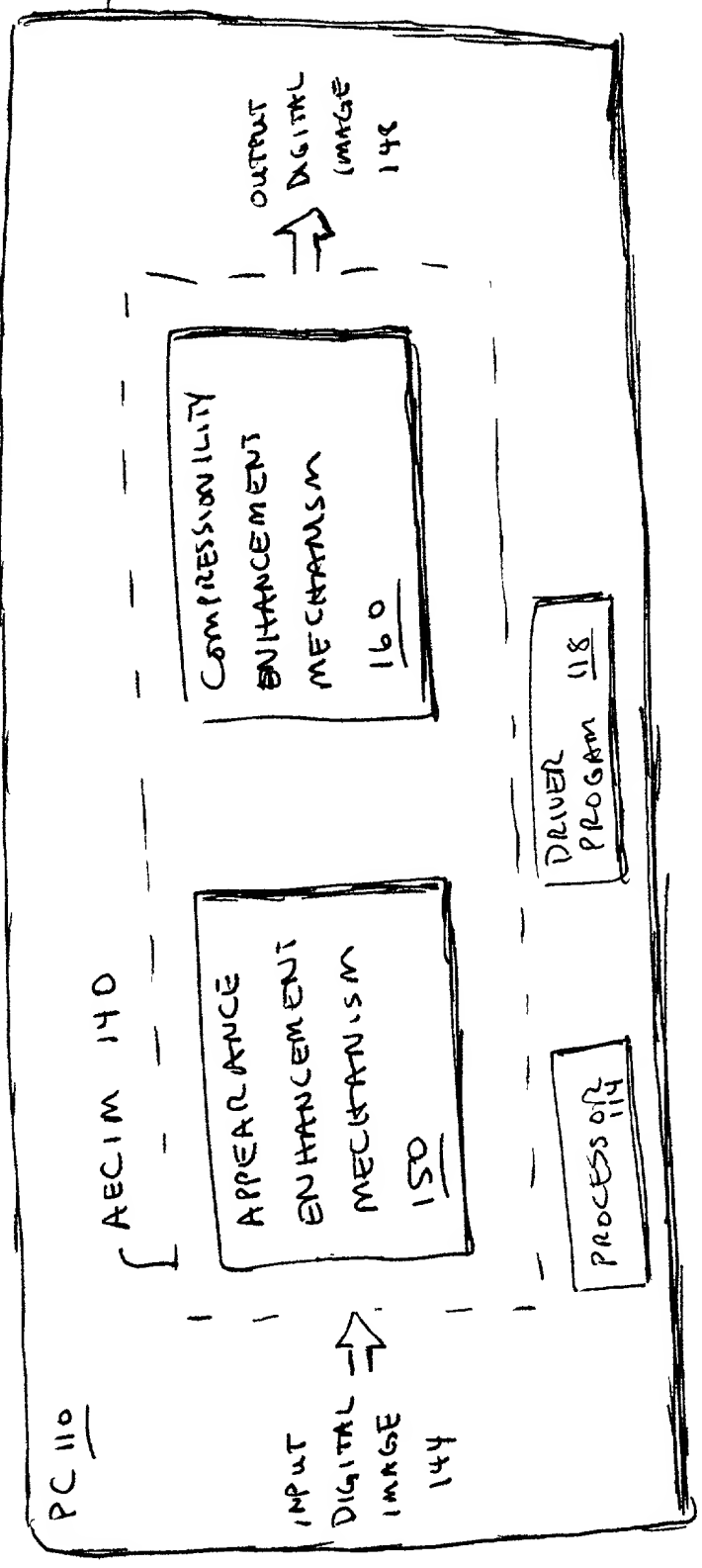


FIG. 1

FIG. 2

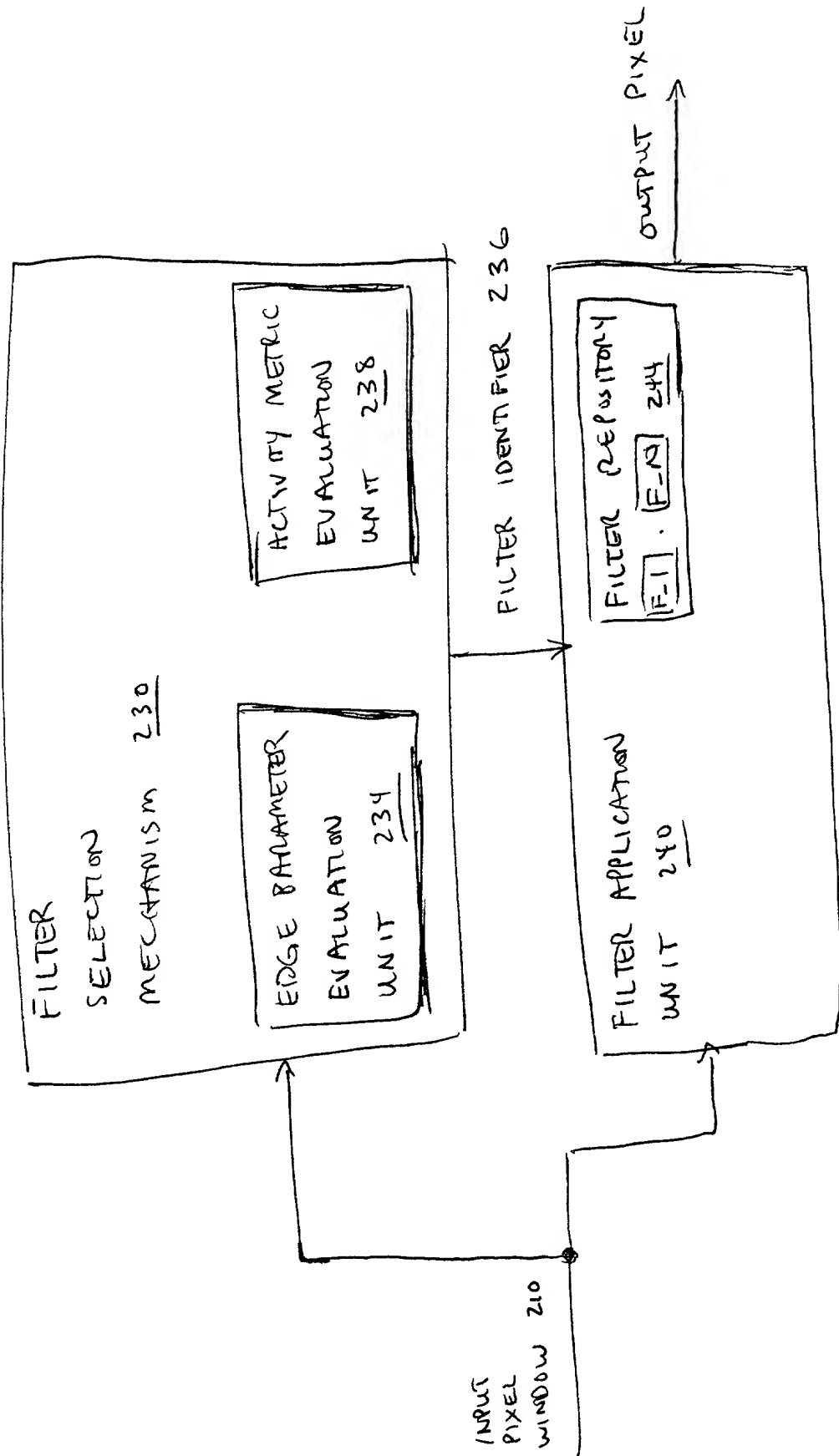


FIG. 2

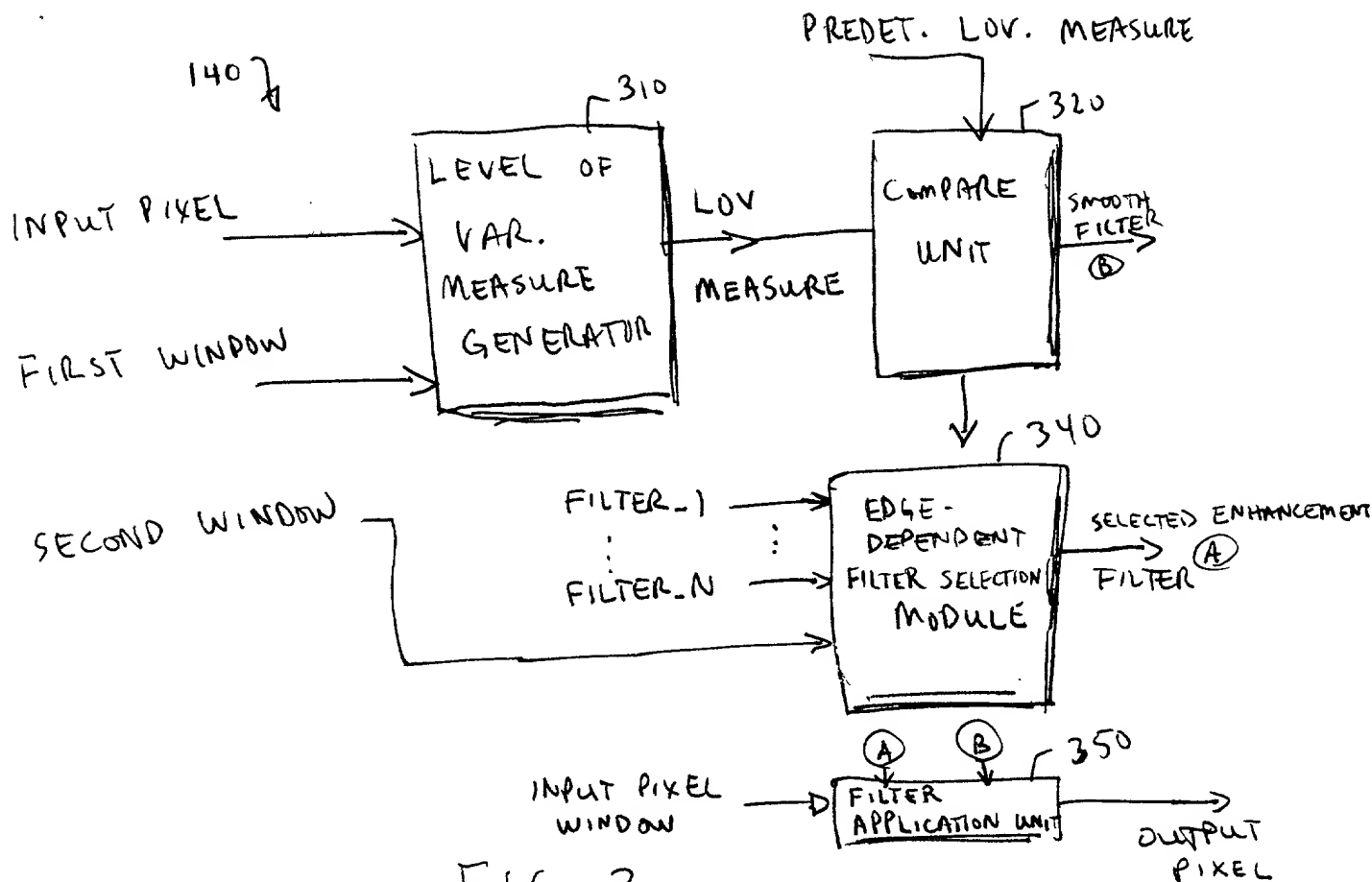


FIG. 3

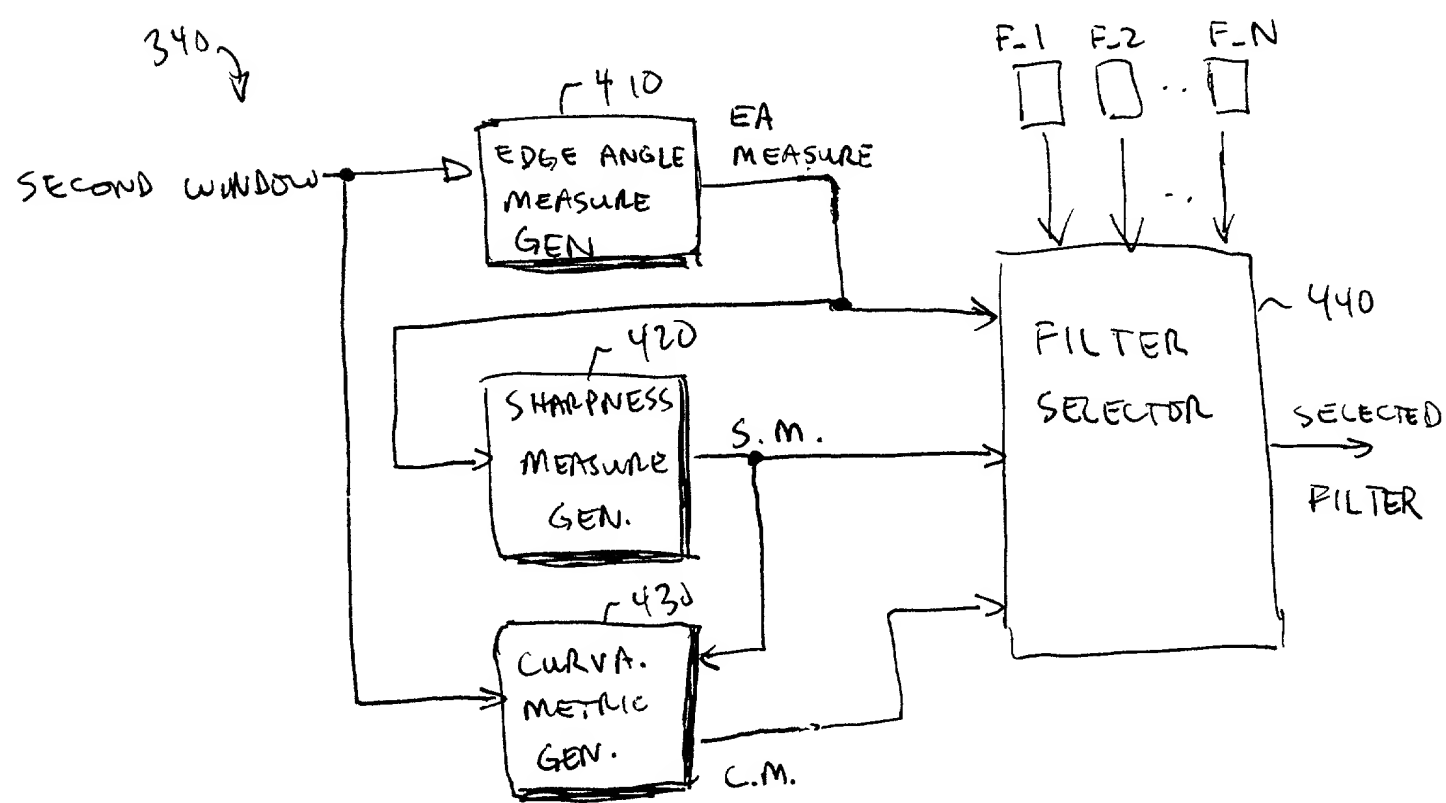


FIG. 4

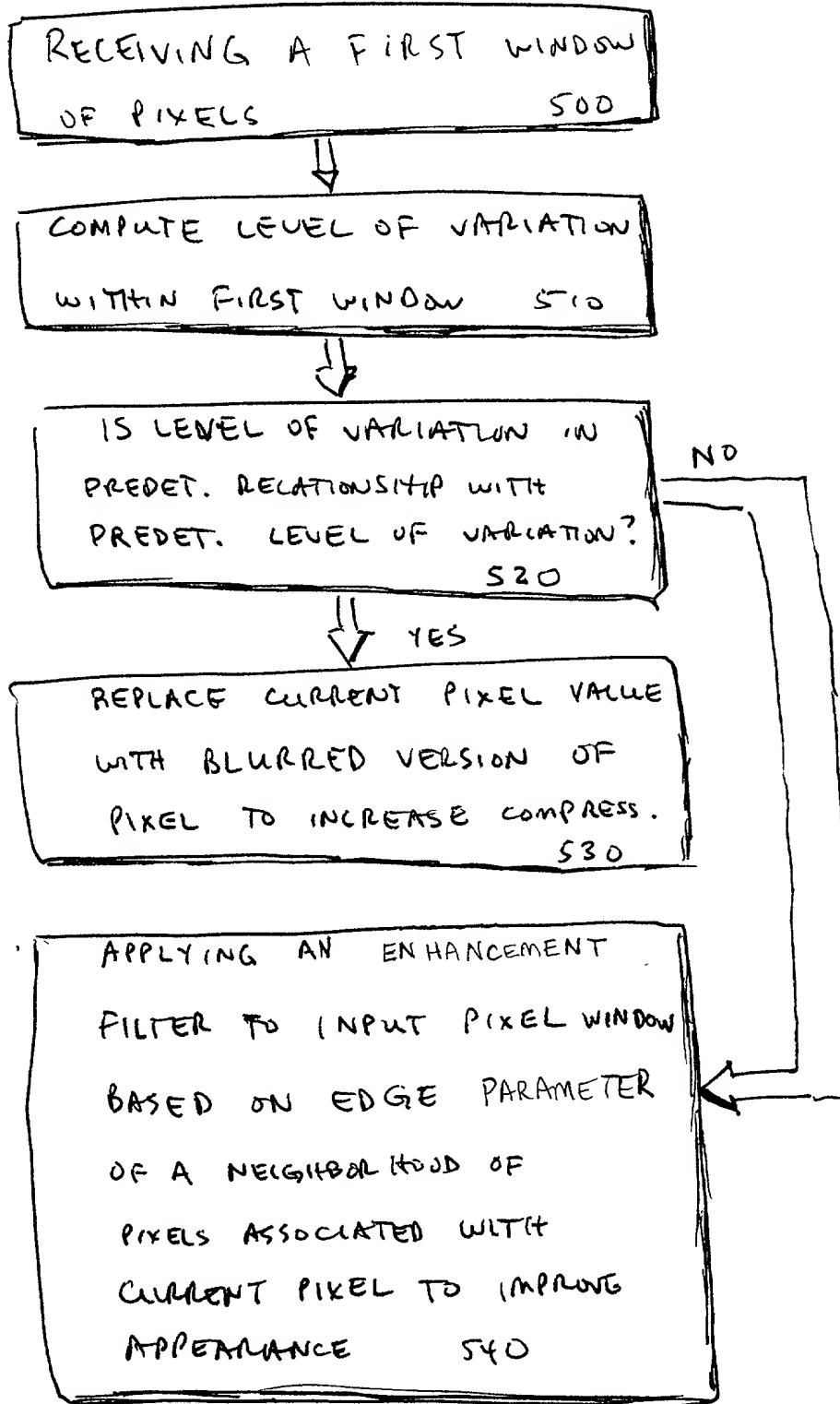


FIG. 5

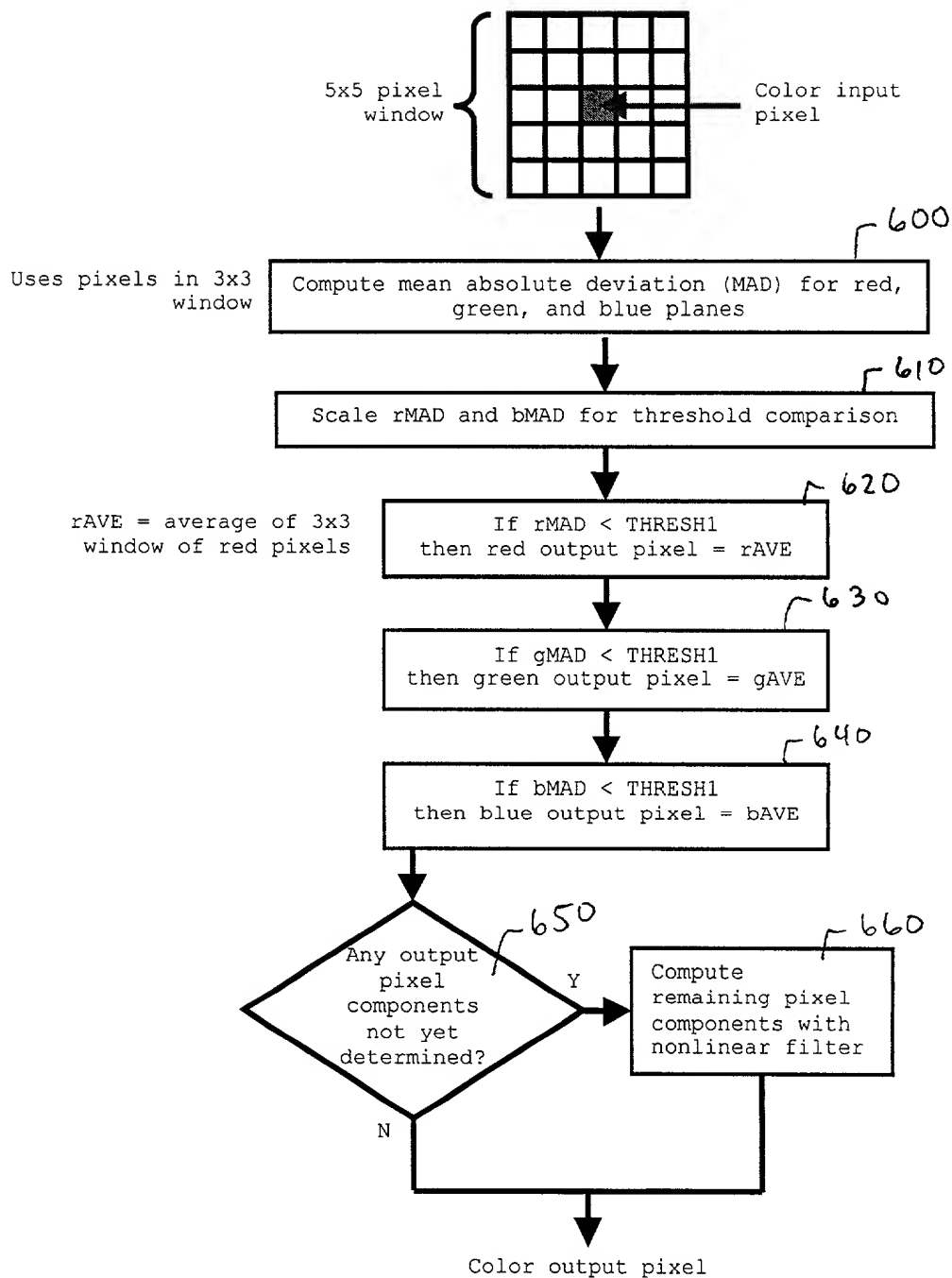


FIG. 6

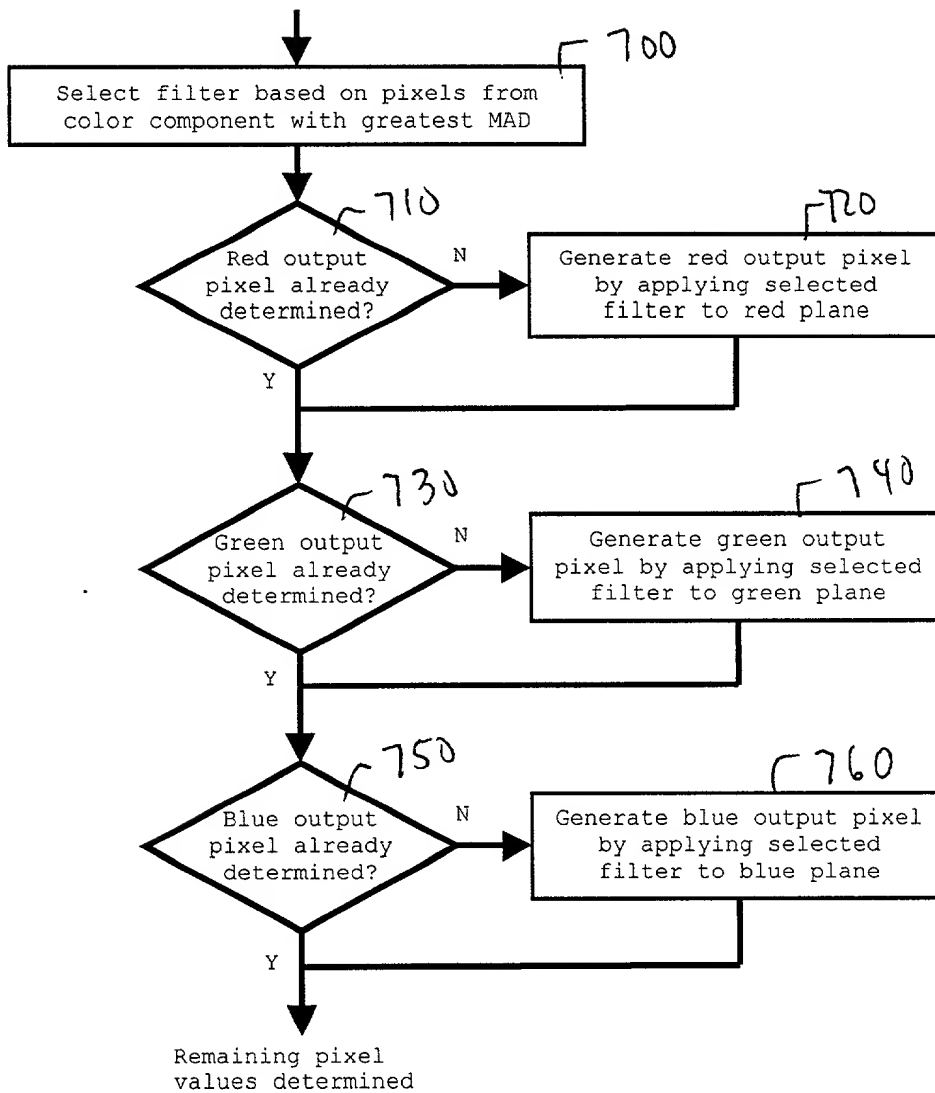


FIG. 7

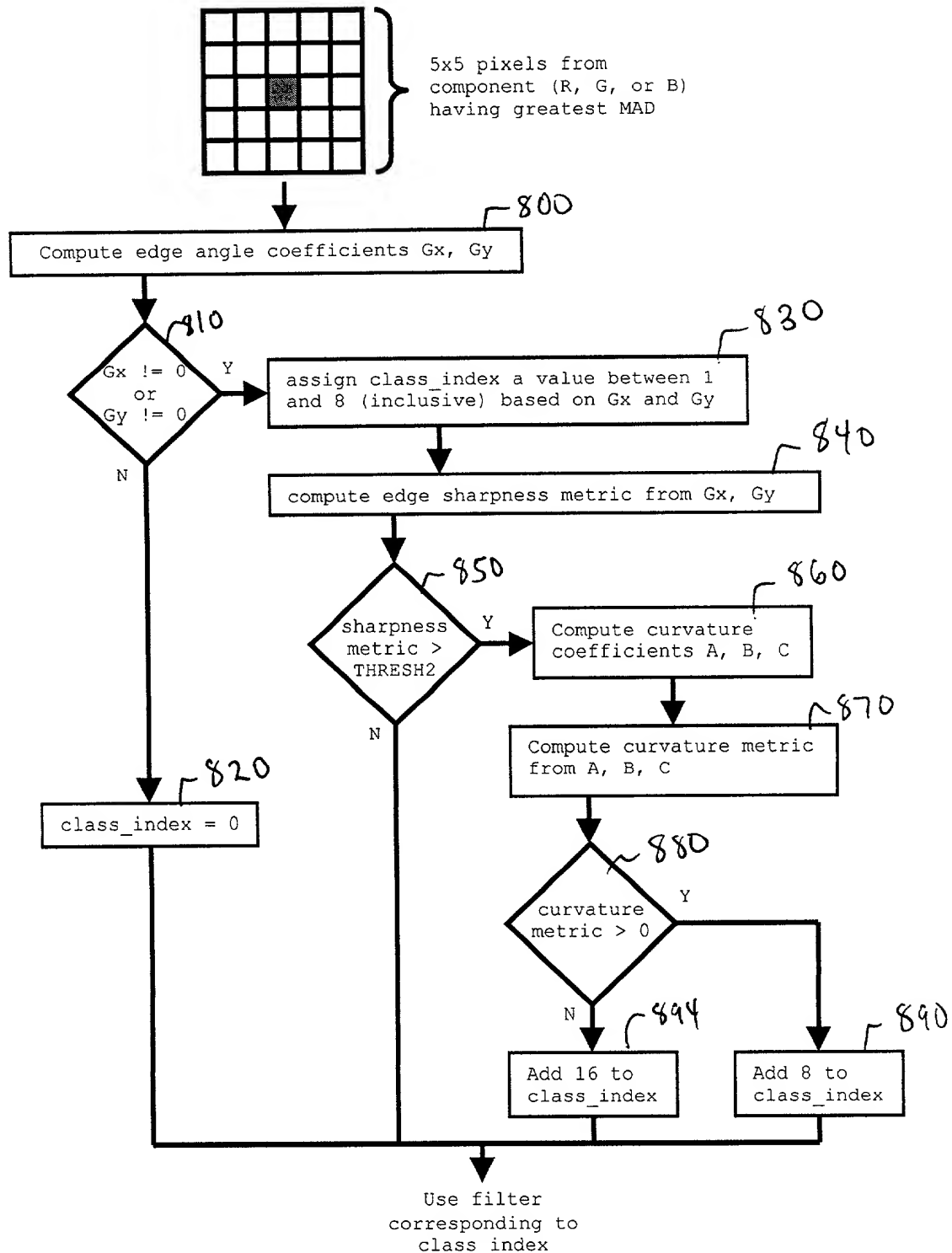


FIG. 8

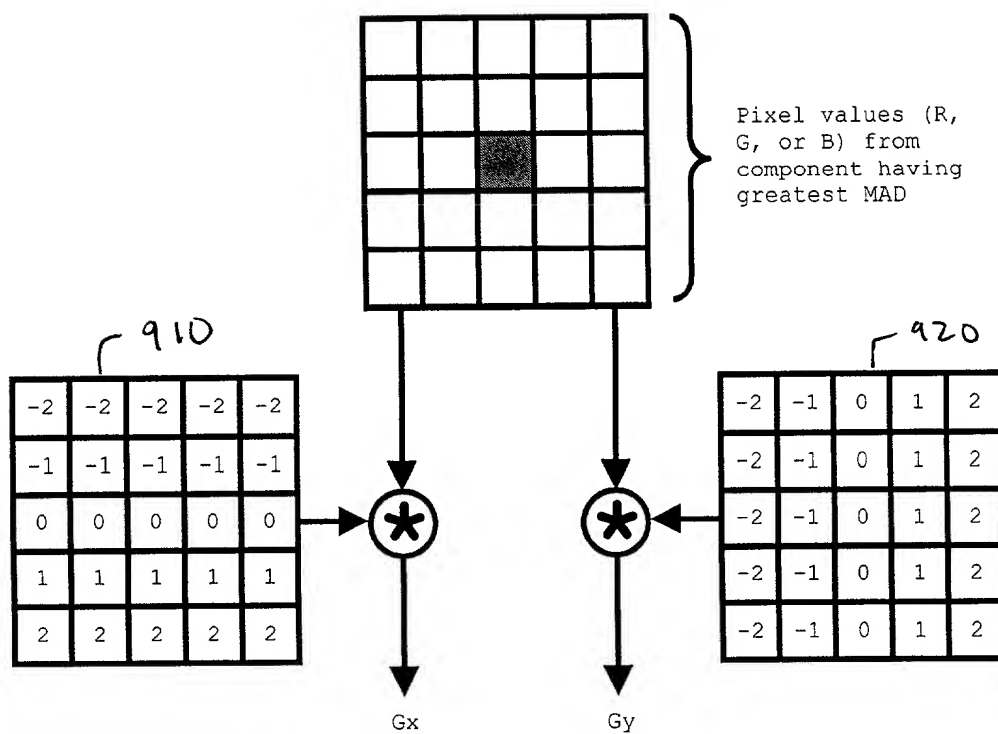


FIG. 9

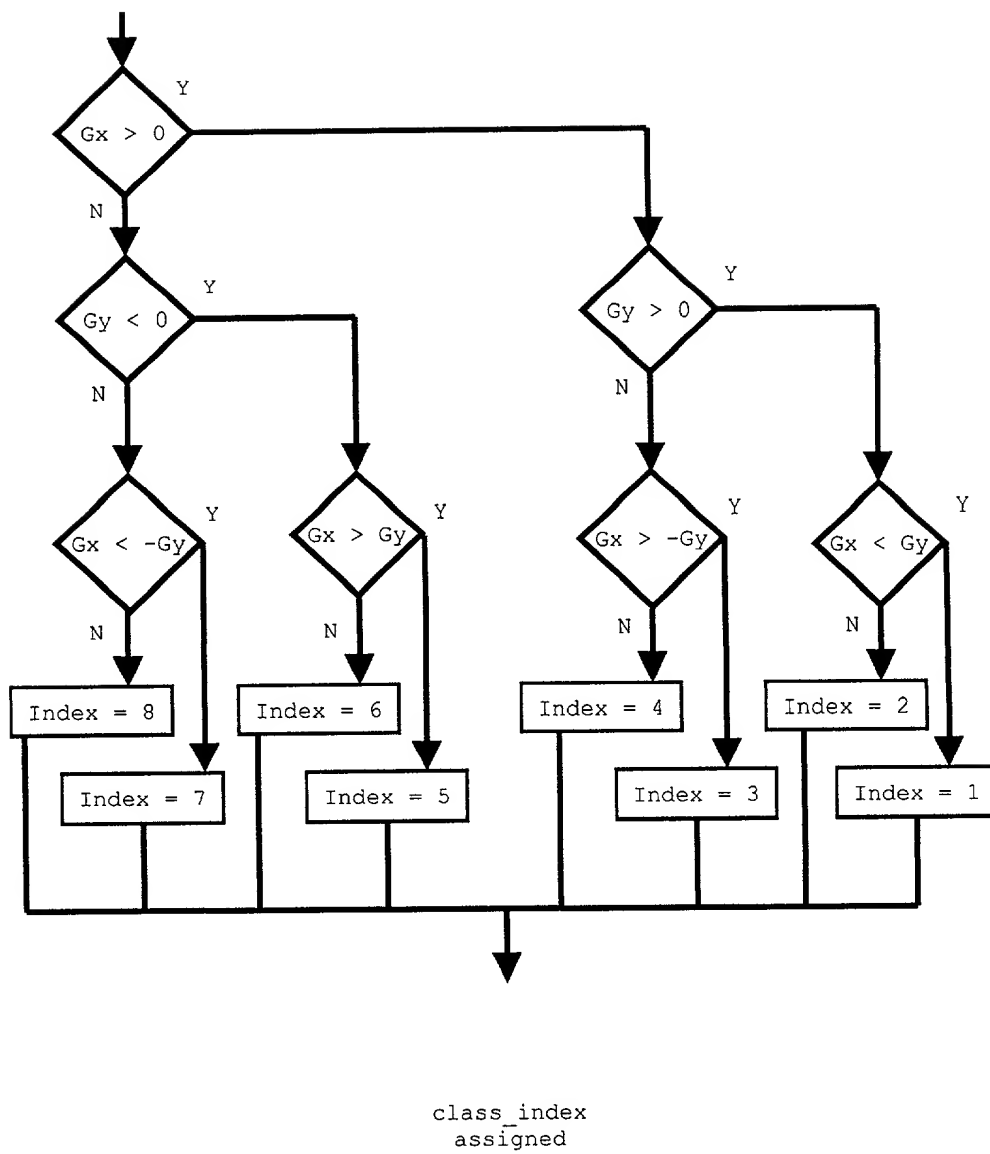


FIG. 10

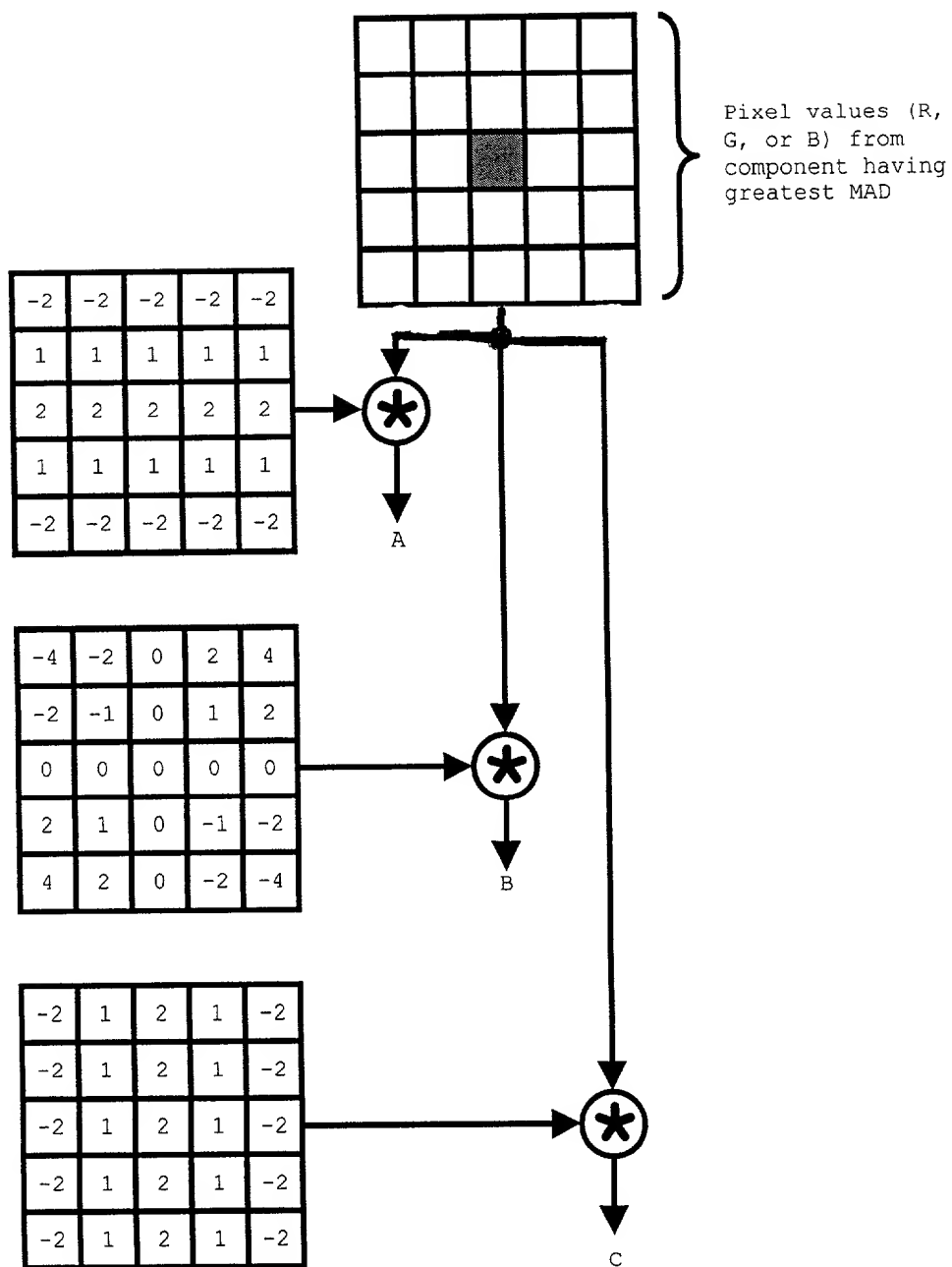


FIG. 11